

**Micro Commercial Components** 



**Micro Commercial Components** 20736 Marilla Street Chatsworth CA 91311 Phone: (818) 701-4933 (818) 701-4939 Fax:

### MJD32C

### Features

- Lead Free Finish/RoHS Compliant ("P" Suffix designates RoHS Compliant. See ordering information) Epoxy meets UL 94 V-0 flammability rating
- Moisture Sensitivity Level 1
- Electrically similar to popular TIP32 Series
- Designed for general purpose amplifier and low speed switching applications.
- Maximum Thermal Resistance: 100°C/W Junction to Ambient

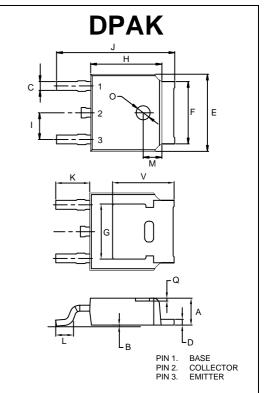
#### Maximum Ratings @ 25°C Unless Otherwise Specified Symbol Rating Rating Unit V<sub>CEO</sub> Collector-Emitter Voltage -100 V Collector-Base Voltage -100 V V<sub>CBO</sub> $V_{EBO}$ Emitter-Base Voltage -5 V -3 Collector Current-Continuous А I<sub>C</sub> 1.25 W Po **Collector Dissipation** Operating Junction Temperature Τ. 150 °C Storage Temperature -65 to +150 TSTG

### Electrical Characteristics @ 25°C Unless Otherwise Specified

Symbol	Parameter	Min	Тур	Max	Units				
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage ( $I_c$ =-30mAdc, $I_B$ =0)	-100			Vdc				
$V_{(BR)CBO}$	Collector-Base Breakdown Voltage (I <sub>c</sub> =-1mAdc, I <sub>E</sub> =0)	-100			Vdc				
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage (I <sub>E</sub> =-1mAdc, I <sub>C</sub> =0)	-5			Vdc				
I <sub>CEO</sub>	Collector Cutoff Current (V <sub>CE</sub> =-60Vdc, I <sub>B</sub> =0)			-50	uAdc				
I <sub>CES</sub>	Collector Cutoff Current (V <sub>CE</sub> =-100Vdc, V <sub>EB</sub> =0)			-20	uAdc				
I <sub>EBO</sub>	Emitter Cutoff Current (V <sub>EB</sub> =-5Vdc, I <sub>C</sub> =0)			-1	mAdc				
h <sub>FE</sub>	DC Current Gain ( $I_C$ =-1Adc, $V_{CE}$ =-4Vdc) ( $I_C$ =-3Adc, $V_{CE}$ =-4Vdc)	25 10		 50					
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage ( $I_c$ =-3Adc, $I_B$ =-0.375Adc) (note 1)			-1.2	Vdc				
$V_{\text{BE(on)}}$	Base-Emitter Voltage (Ic=-3Adc, Vc=-4Vdc) (note 1)			-1.8	Vdc				
f <sub>T</sub>	Transition frequency (V <sub>CE</sub> =-10Vdc,Ic=-0.5Adc,fr=1KHz)	3			MHz				

Note: 1. Pulse Test: PW≤300µs, Duty Cycle≤2%

# Silicon **PNP** epitaxial planer **Transistors**



		DINILI	VSIONS		<u> </u>
	INCHES MM		1M		
DIM	MIN	MAX	MIN	MAX	NOTE
Α	0.087	0.094	2.20	2.40	
В	0.000	0.005	0.00	0.13	
С	0.026	0.034	0.66	0.86	
D	0.018	0.023	0.46	0.58	
E	0.256	0.264	6.50	6.70	
F	0.201	0.215	5.10	5.46	
G	0.190		4.83		
Н	0.236	0.244	6.00	6.20	
	0.086	0.094	2.18	2.39	
J	0.386	0.409	9.80	10.40	
К	0.114		2.90		
L	0.055	0.067	1.40	1.70	
М	0.063		1.60		
0	0.043	0.051	1.10	1.30	
Q	0.000	0.012	0.00	0.30	
V	0.211		5.35		

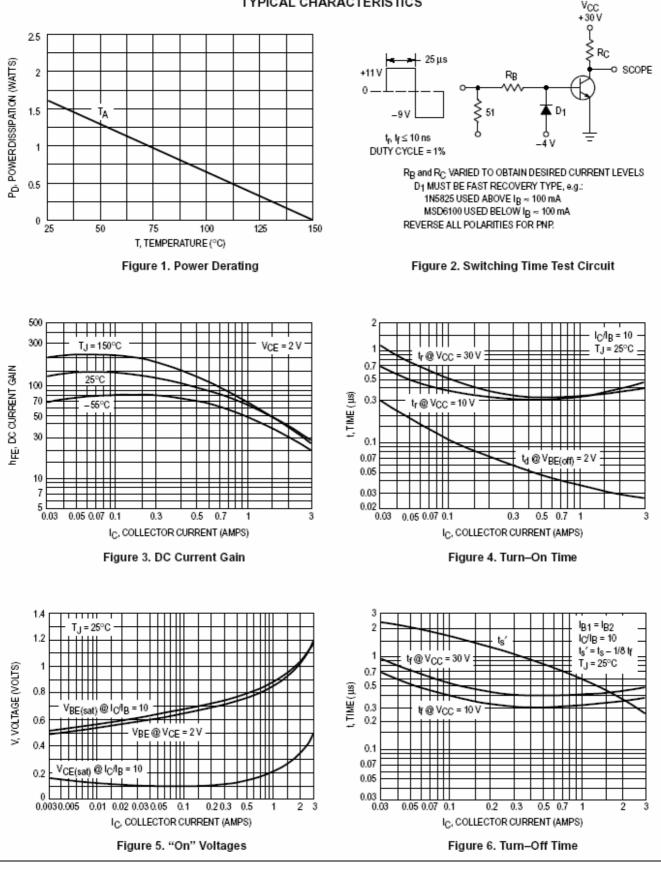
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# MJD32C



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#### TYPICAL CHARACTERISTICS



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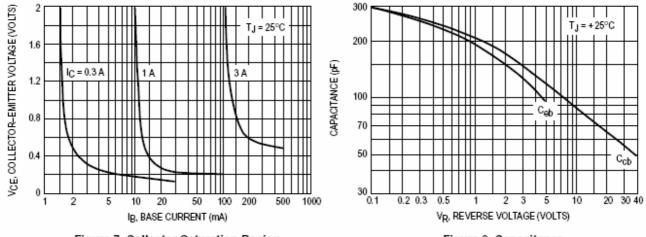
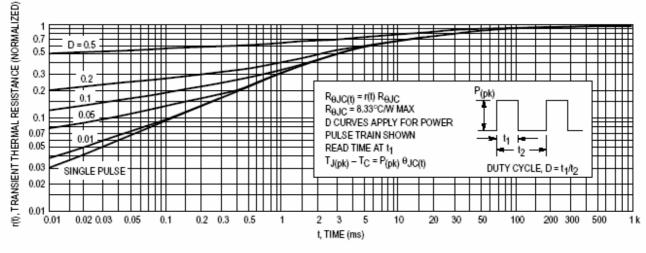




Figure 8. Capacitance





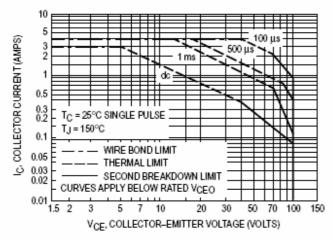


Figure 10. Active Region Safe Operating Area

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate  $I_{\rm C} - V_{\rm CE}$  limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 10 is based on  $T_{J(pk)} = 150^{\circ}C$ ;  $T_C$  is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided  $T_{J(pk)} \le 150^{\circ}C$ .  $T_{J(pk)}$  may be calculated from the data in Figure 9. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

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### **Ordering Information :**

Device	Packing
Part Number-TP	Tape&Reel: 2.5Kpcs/Reel

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